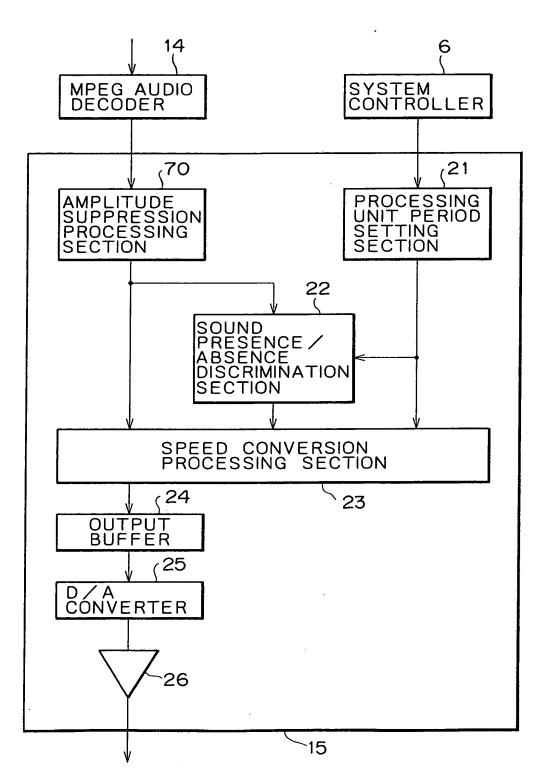
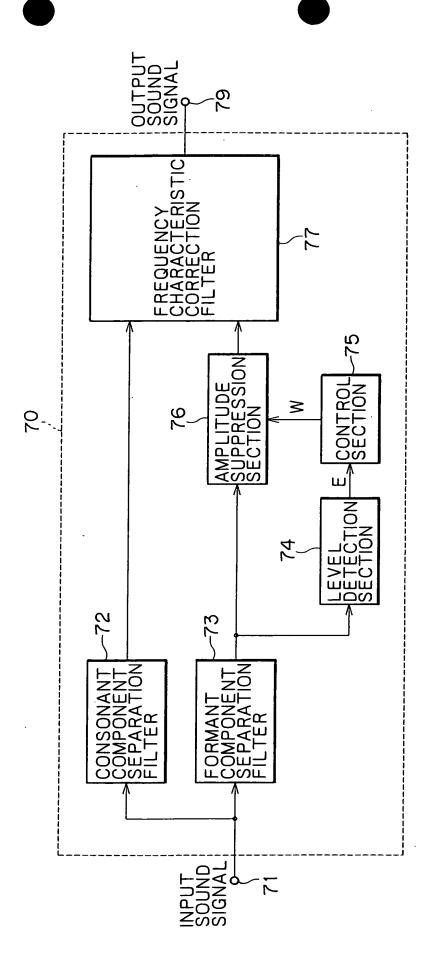
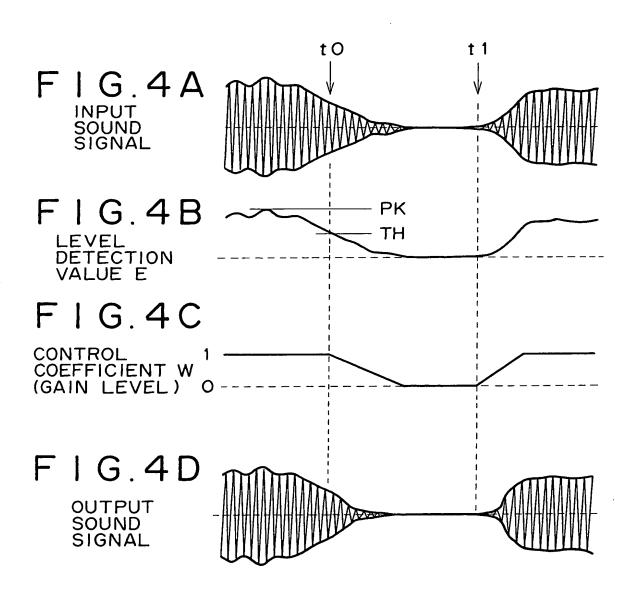


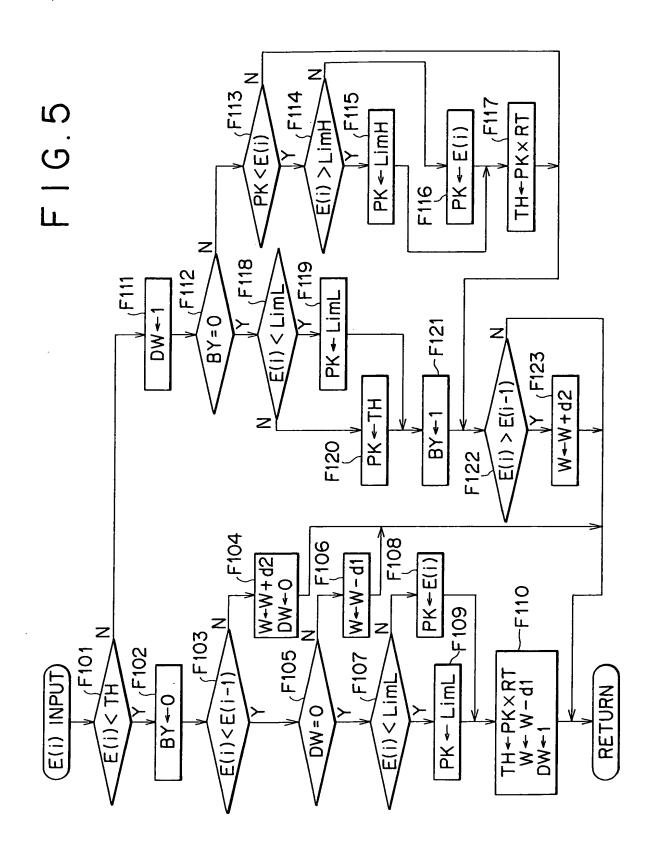
F I G. 2



F | G. 3







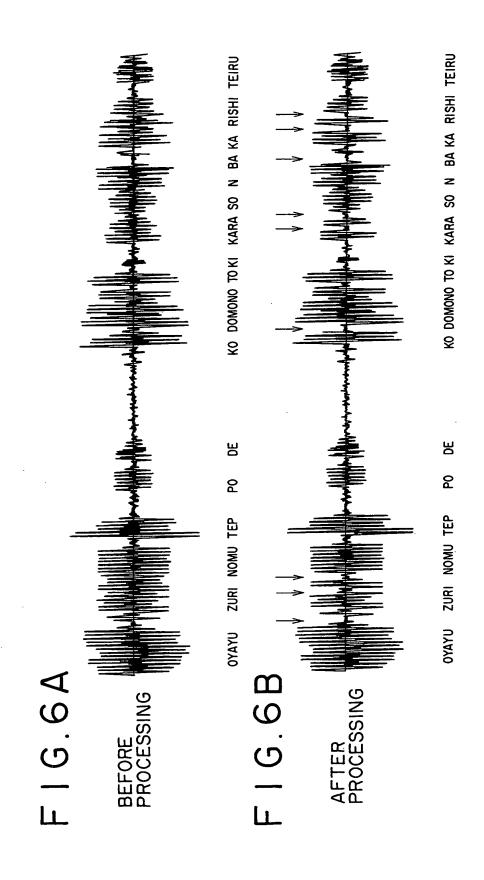
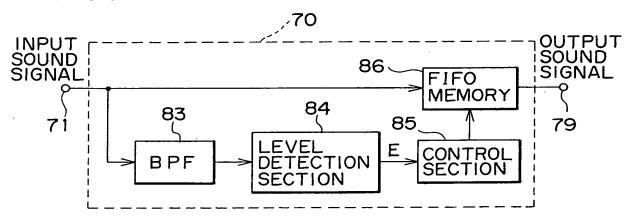
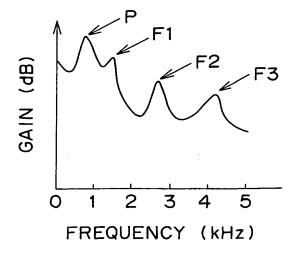


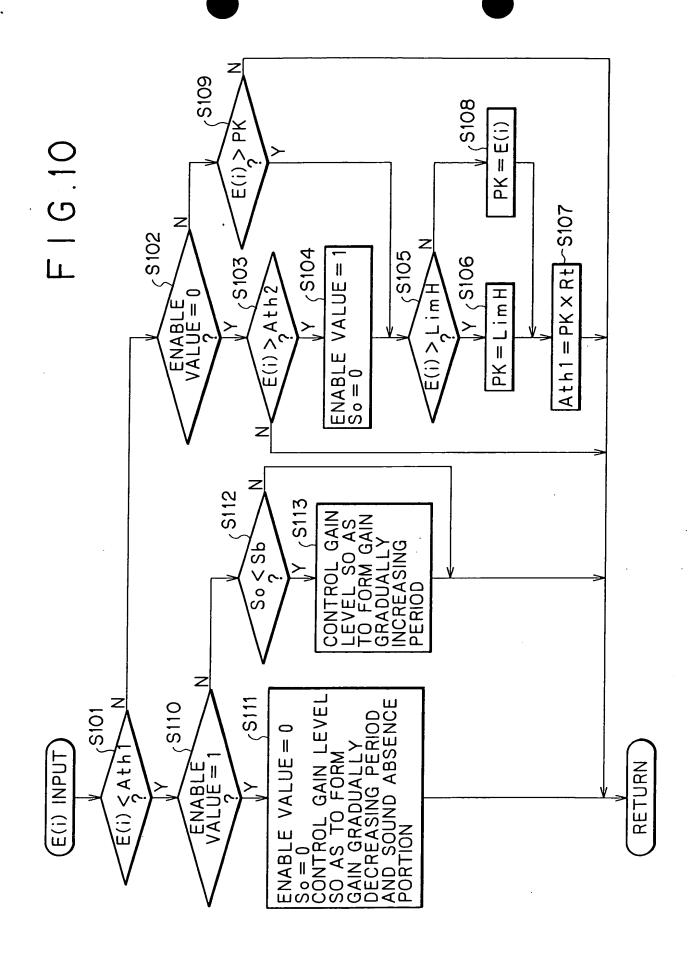
FIG.7 ,70 OUTPUT INPUT SOUND SOUND SIGNAL 81 82 **AMPLITUDE** SIGNAL DELAY CIRCUIT SUPPRESSION SECTION 84 83 71 79 85 LEVEL DETECTION SECTION CONTROL BPF

F I G. 8



F I G. 9





F | G.11

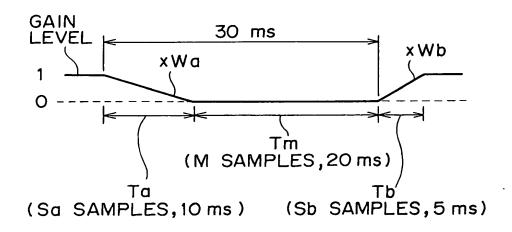


FIG.12A

INPUT SOUND SIGNAL

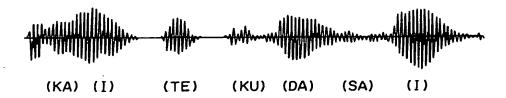
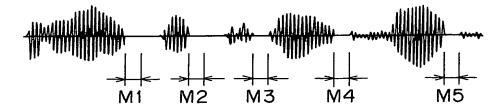
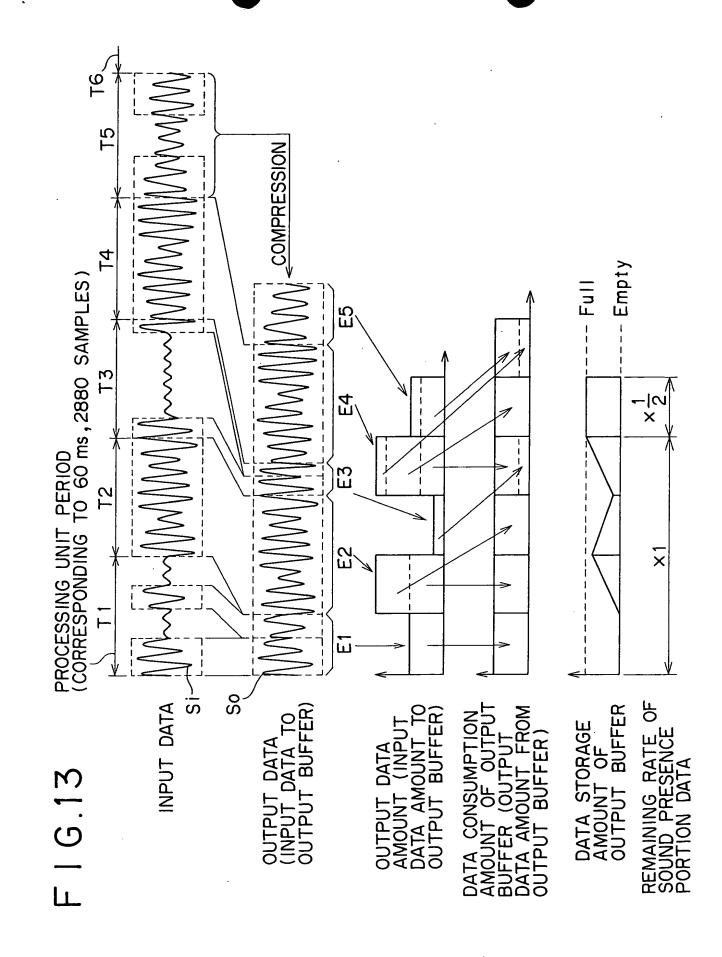
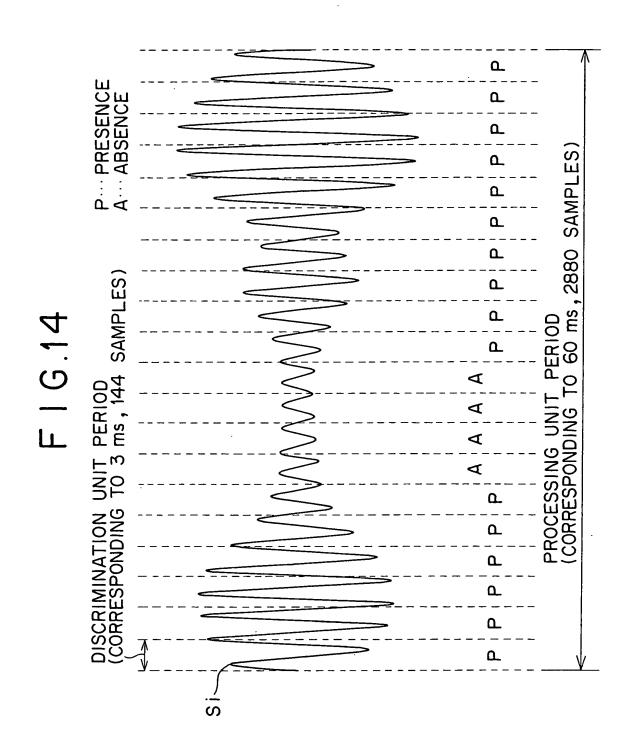


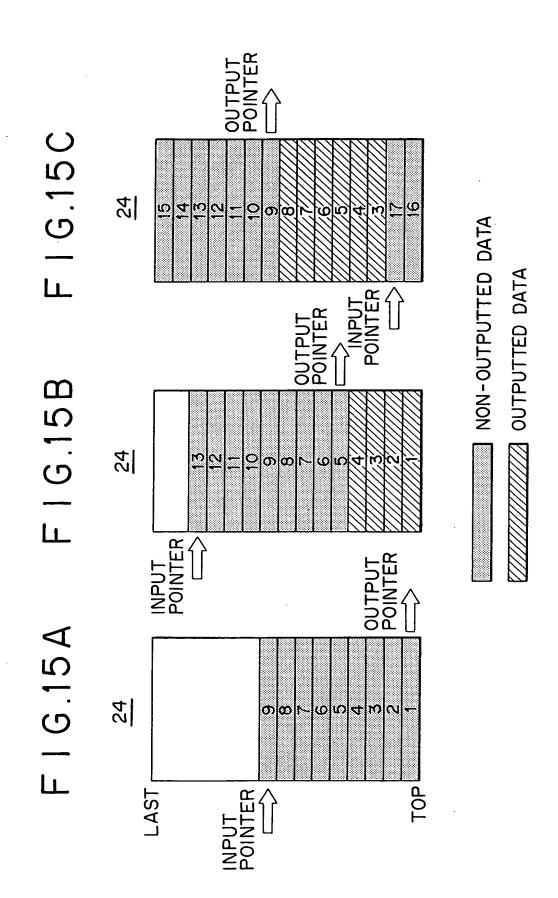
FIG.12B

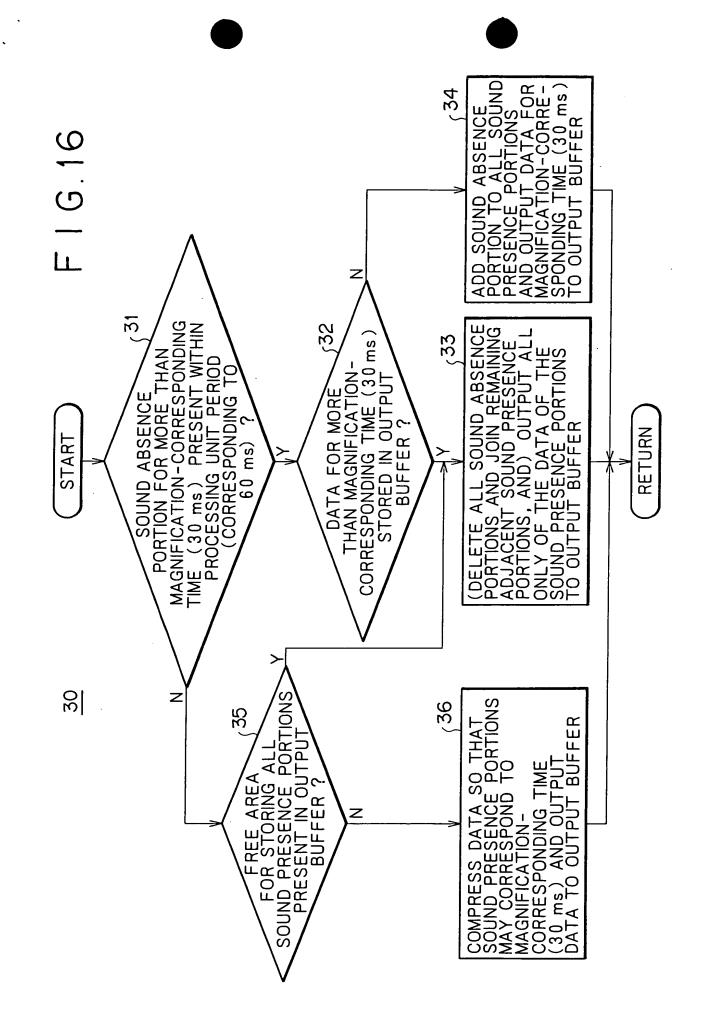
OUTPUT SOUND SIGNAL

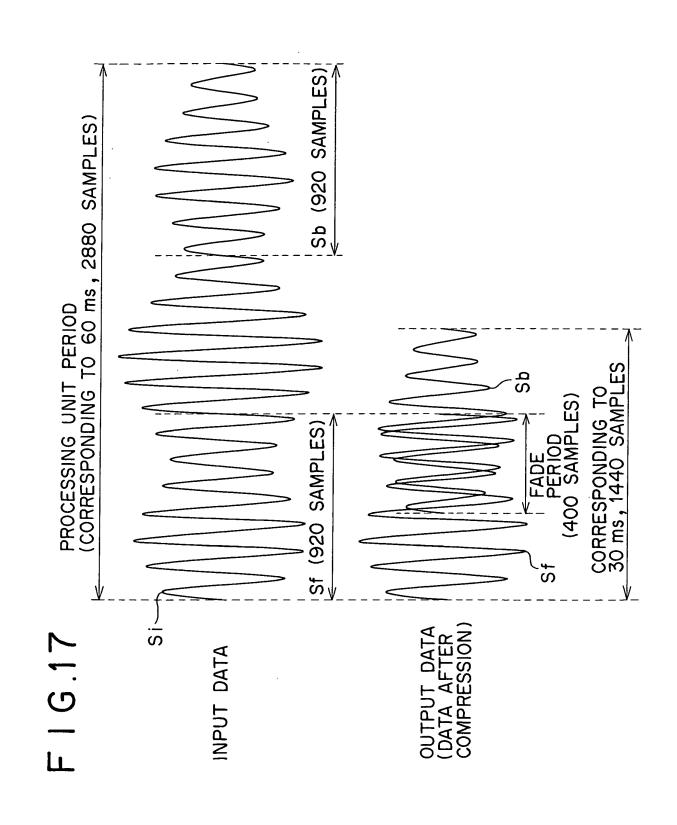


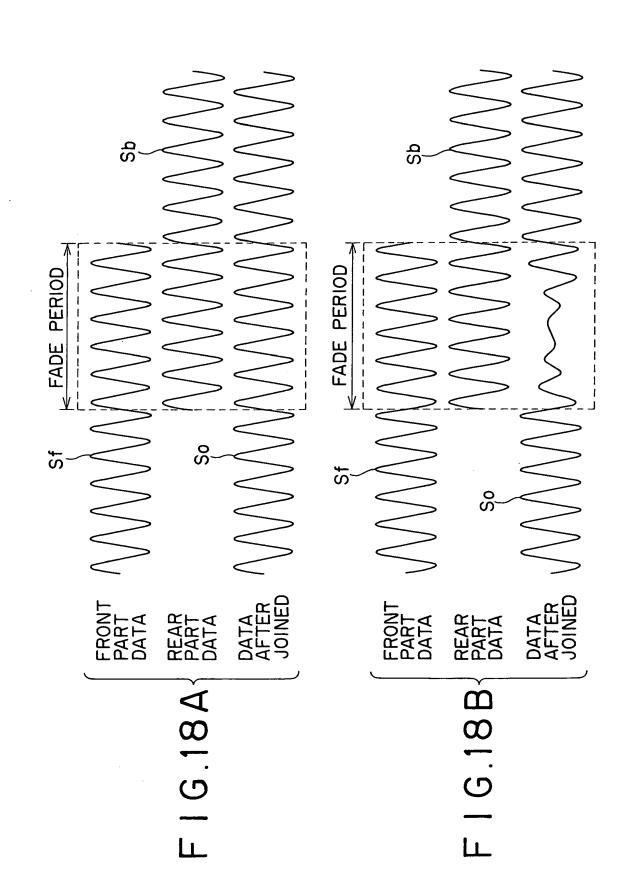




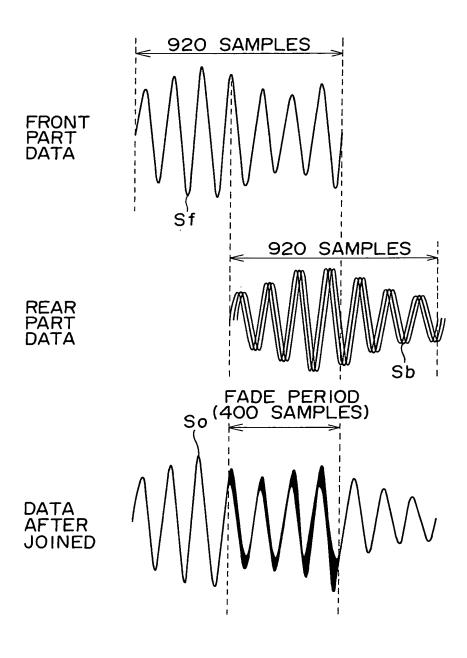


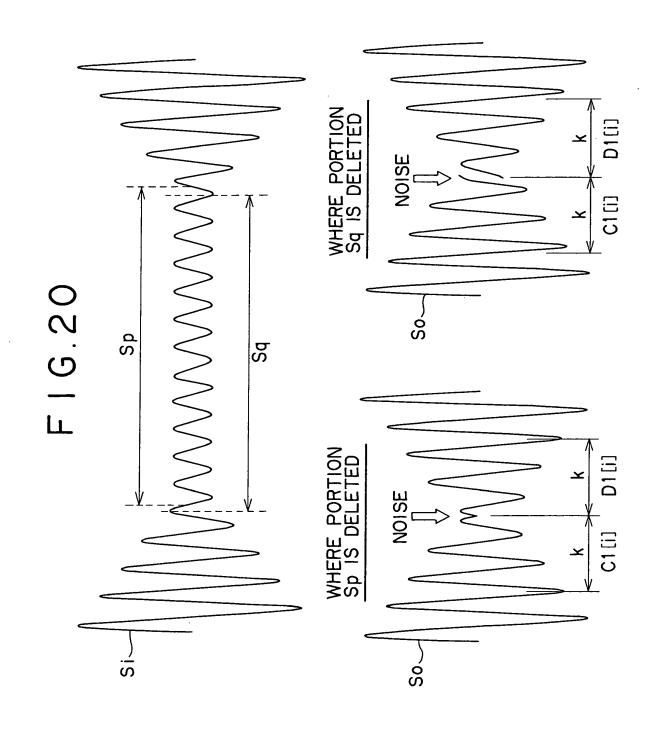




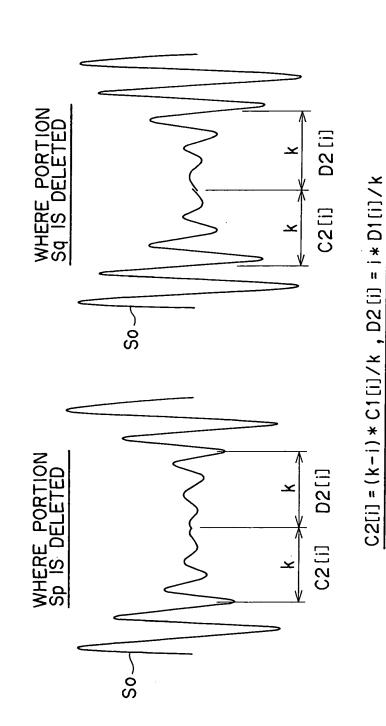


F I G.19

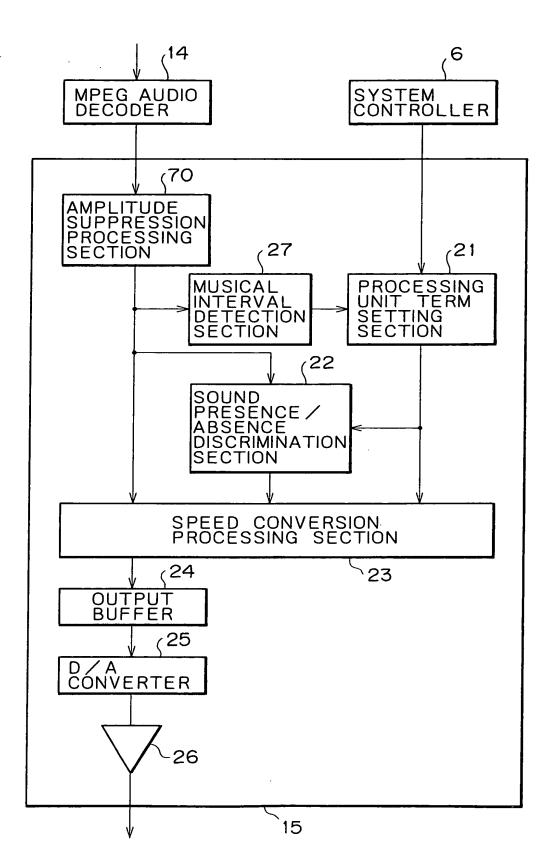


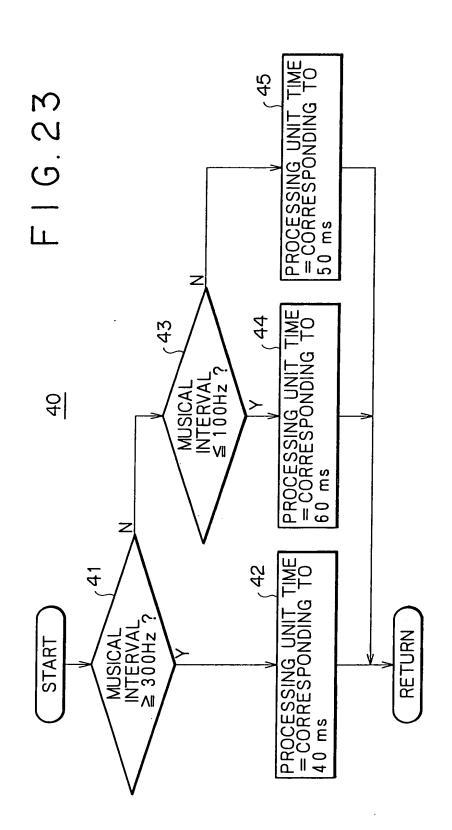


F16.21

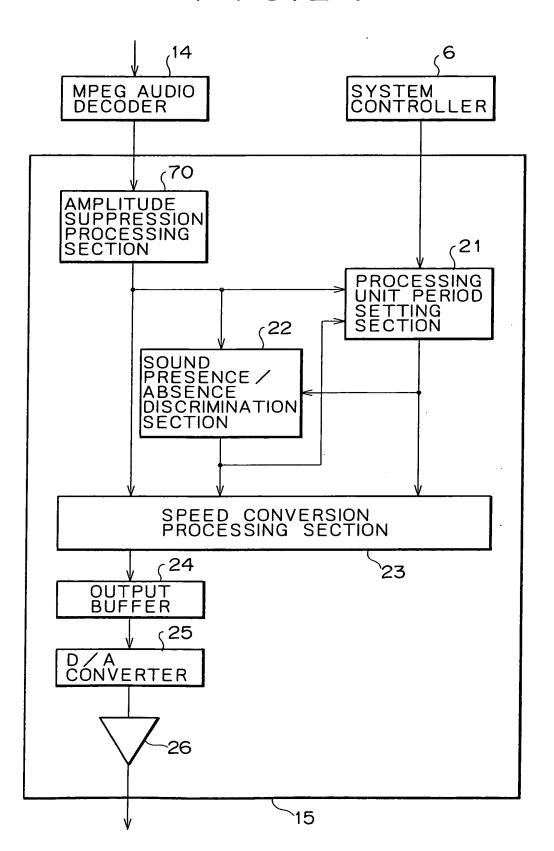


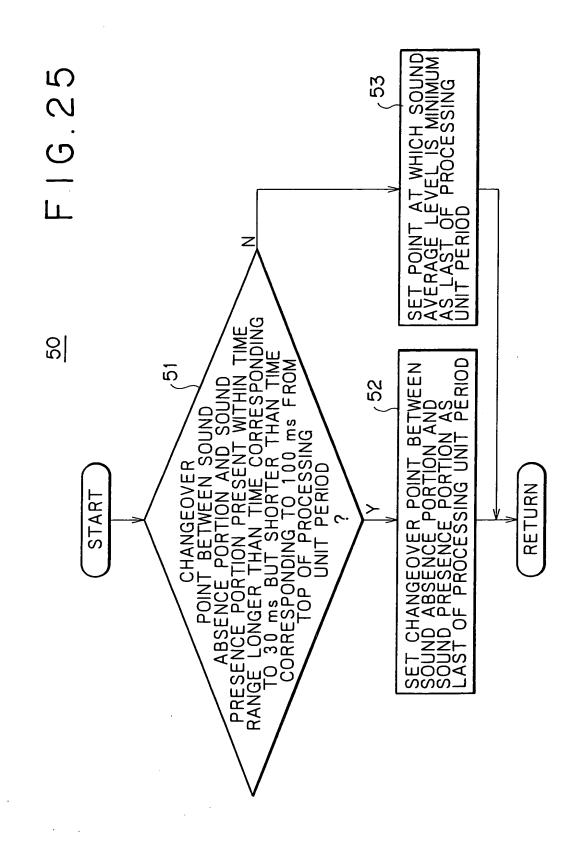
F1G.22





F I G. 24

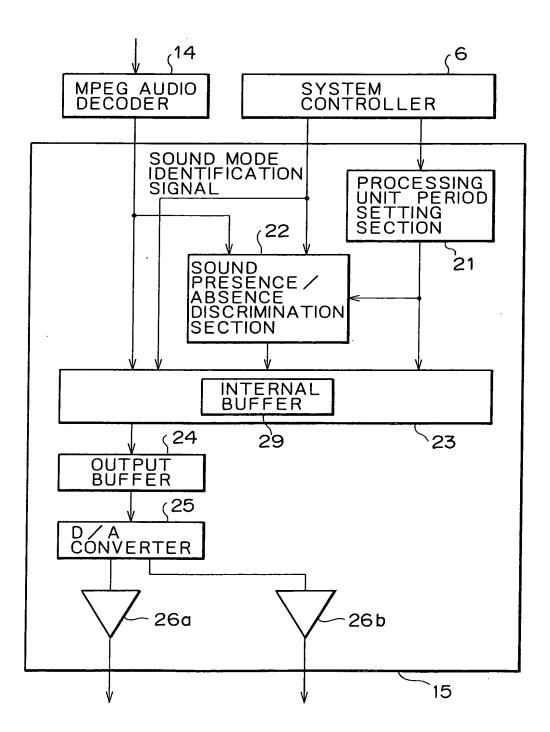




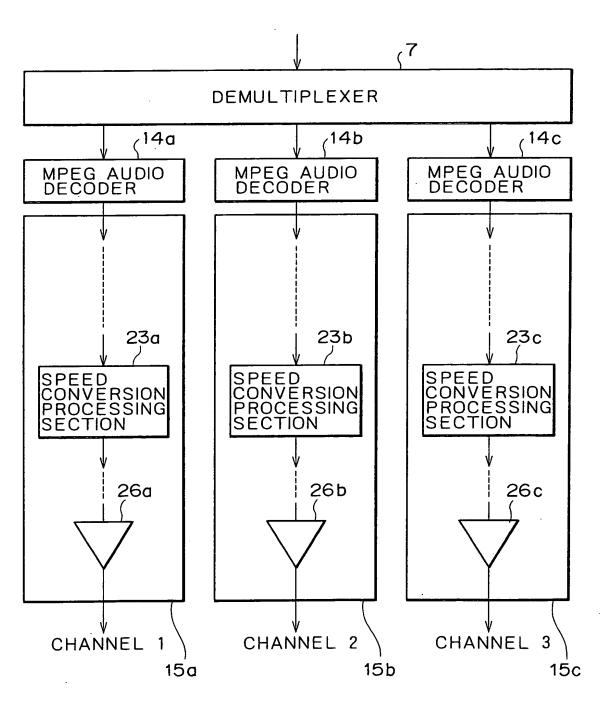
F16.26

TIME 240 ms 200 ms **T**4 **T14** 180 ms PRESENCE → ABSENCE → PRESENCE **T**3 120 ms tp 100 ms 12 60 ms ta 30 ms O ms IN CASE OF VARIABLE LENGTH

F1G.27



F I G.28



F1G.29

